

P-2.1 Subcommittee on Ceramic Dielectric Capacitors

AGENDA

Wednesday, October 6, 2004

Holiday Inn on the Bay
1355 North Harbor Drive
San Diego, CA 92101

1.0 Introductions

1.1 Circulate Membership Roster

1.2 Approval of Spring Meeting Minutes from Tampa, FL.

1.3 Review of Agenda for Present Meeting

1.4 Correspondence & Review of Committee's Scope

1.5 Report of task groups, committees, and DSCC efforts

1. DSCC
2. ACH
3. U-1
4. STC

2.0 Old Business

2.1 Status of balloting latest revision of EIA-595-A (SP-4622)

2.2 Status of EIA-469-D (PN-4621) SP ballot

2.3 Status of EIA-198-F "Ceramic Dielectric Capacitors Classes I, II, III, & IV"

2.3.1 Part II-Test Methods

2.3.2 Part III- Individual Specs.

2.3.2.1 Status of SMD High Voltage

2.3.2.2 Status of Leaded High Voltage

2.3.2.3 Status of Reverse Geometry Low Inductance

2.4 Status of IEC/EIA 521

2.5 Status of ESL Capacitor test method

2.5.1 Update on JEITA activities

2.6 Report from Ceramic Capacitor Working Group (meeting 10/04/04, 10am-2pm)

2.6.1 Low voltage component bulletin

2.6.2 IEC comparison to EIA's historical approach

2.7 Status on Electro-static Discharge (ESD) teleconference

2.8 Active projects document file tracking method.

3.0 New Business

3.1 ECA Engineering Bulletin posting of tin whisker position statement

3.2 Working Group status report and proposed agenda items for next meeting.

3.3 Correct sub-committee name on all correspondence.

4.0 Time and Place of next meeting

5.0 Adjournment